

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
1	BRS	L1	1	"6804791".pn.	US- PGPUB; USPAT	2004/12/10 18:42		
2	BRS	L2	1	1 and power	US- PGPUB; USPAT	2004/12/10 19:06		
3	BRS	L3	1	"6535985".pn.	US- PGPUB; USPAT	2004/12/10 19:03		
4	BRS	L4	1	3 and power	US- PGPUB; USPAT	2004/12/10 19:04		
5	BRS	L5	3	"6795929".pn. "6792552".pn. "6782483".pn.	US- PGPUB; USPAT	2004/12/10 19:07		
6	BRS	L6	3	5 and power	US- PGPUB; USPAT	2004/12/10 19:11		
7	BRS	L7	52055	power near5 (processor microprocessor cpu mpu)	US- PGPUB; USPAT	2004/12/10 19:12		
8	BRS	L8	30541	(stop\$4 sleep) same clock	US- PGPUB; USPAT	2004/12/10 19:13		
9	BRS	L9	10172	(low\$2 near5 power) same clock	US- PGPUB; USPAT	2004/12/10 19:14		
10	BRS	L10	1178	7 same 8	US- PGPUB; USPAT	2004/12/10 19:14		
11	BRS	L11	1264	7 same 9	US- PGPUB; USPAT	2004/12/10 19:14		
12	BRS	L12	1884	10 or 11	US- PGPUB; USPAT	2004/12/10 19:14		
13	BRS	L13	419641	(input\$4 receiv\$3) near3 data	US- PGPUB; USPAT	2004/12/10 19:15		
14	BRS	L14	1060	12 and 13	US- PGPUB; USPAT	2004/12/10 19:16		
15	BRS	L15	248646	(start\$3 wak\$3 initiat\$3) near5 (microprocessor process\$3)	US- PGPUB; USPAT	2004/12/10 19:18		

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
16	BRS	L16	565	14 and 15	US- PGPUB; USPAT	2004/12/10 19:18		
17	BRS	L17	65647	transition\$3 near5 (state mode)	US- PGPUB; USPAT	2004/12/10 19:19		
18	BRS	L18	180	16 and 17	US- PGPUB; USPAT	2004/12/10 19:20		
19	BRS	L19	0	"713".300-320.ccls.	US- PGPUB; USPAT	2004/12/10 19:20		
20	BRS	L20	2238	713/300-320.ccls.	US- PGPUB; USPAT	2004/12/10 19:21		
21	BRS	L21	1376	713/322-324.ccls.	US- PGPUB; USPAT	2004/12/10 19:21		
22	BRS	L22	3129	20 or 21	US- PGPUB; USPAT	2004/12/10 19:21		
23	BRS	L23	81	18 and 22	US- PGPUB; USPAT	2004/12/10 19:29		
24	BRS	L24	99	18 not 23	US- PGPUB; USPAT	2004/12/10 19:30		